h.makaryan@ysu.am

Institute of Physics
Chair of Physical Process Modeling and Signal Processing
Lecturer on an hourly basis

Institute of Physics
Chair of Physical Process Modeling and Signal Processing
Lecturer on an hourly basis

Publications

Article

IFLEX: A Fully Open-Source, High-Density Field-Programmable Gate Array (FPGA)-Based Hardware Co-Processor for Vector Similarity Searching

Ludovico Minati, Vardan Movsisyan, Matthiew McCormick, Khachatur Gyozalyan, Tigran Papazyan, Hrach Makaryan, Stefano Aldrigo, Taron Harutyunyan, Hayk Ghaltaghchyan, Chris McCormick, Mick Fandrich

IEEE Access 2019 8794567